

FIG. 1

Version no. of structure	Allows PP to check format
PP -> NP message queue	
NP→ PP message queue	
Flow table addresses	Used by NP to find flows
NP flow handler routine addresses	Used by PP when setting up flows
Debug & monitoring	NP cpu meter. NP register dump
Written by PP Written by N	NP

FIG. 4

7 state variables (to be preloaded into registers) Used for current buffer pointers, cell counts, policing params, ect.

First part has a similar format in all follows. A flow is invoked by a single instruction:

- loads 8 or 9 registers
- jumps to handler routine

NP rx handler address

NP tx handler address

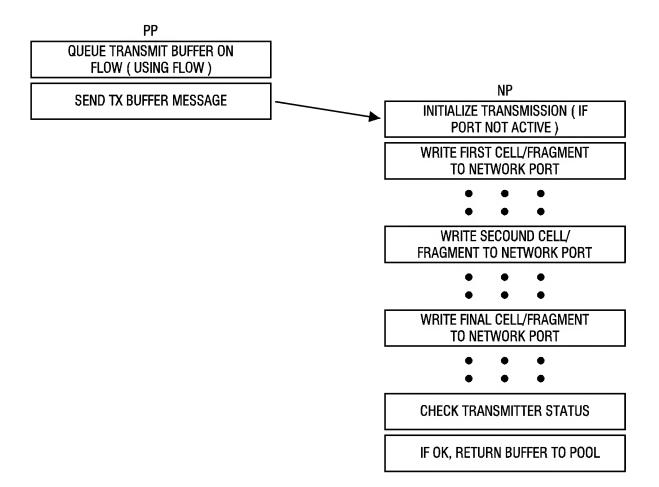
Current buffer

Buffer source and/or destination

Type. Flags

Local buffer queue (switch flows)

Other flow-specific data



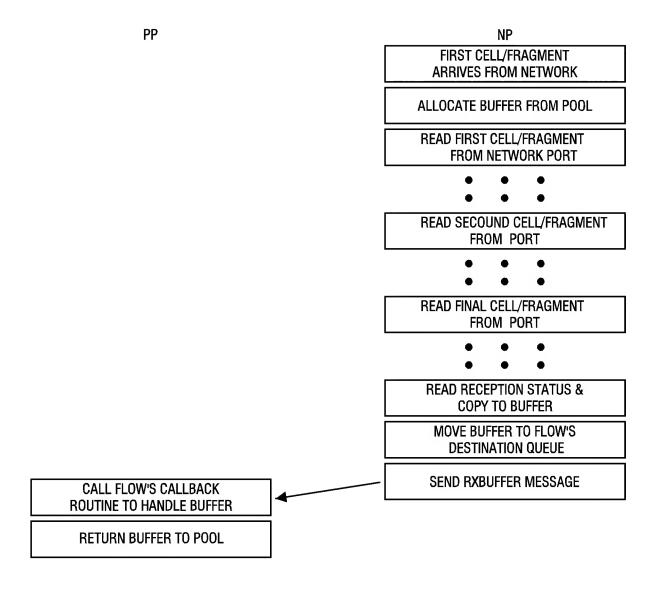
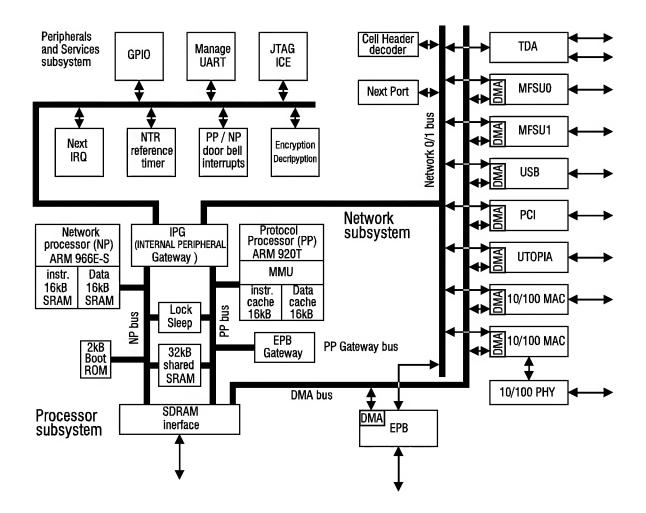
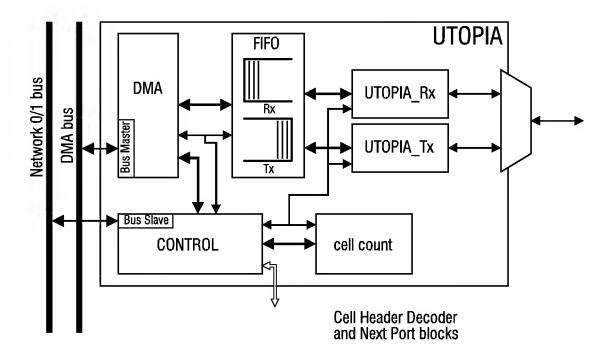


FIG. 7

OCTETS field in	Least significant 2	Keep ALIGN	TEXT_ALIGN register	Next word from memory	TEXT_ALIGN register word	Word written to FIFO
TX ALIGN	bite of DMA	flag	word at	non memory	after first	101110
field in	adress	liay	start		memory cycle	
XX	00	0	XXXX XXXX	pgrs . vwyz	XXXX XX00	pqrs . vwyz
$-\widetilde{\mathbf{x}}$	01	Ö	XXXXXXXXX	pqrs . vwyz	pgrs . vw01	No write
$\overline{\mathbf{x}}$	10	l ő	XXXX : XXXX	pgrs . XXXX	pgrs . vw02	No write
XX	11	0	XXXX : XXXX	pqXX . XXXX	pqXX . vw03	No write
		 	70001.70001	pqrvt.rvvvt	pquittio	NO WILLO
00	00	1	XXXX . XX00	pqrs . vwyz	XXXX . XX00	pqrs . vwyz
00	01	1 i	XXXX XXXX	pgrs . vwXX	pgrs . vw01	No write
00	10	 	XXXX . XX00	pgrs . XXXX	pgrs . vw02	No write
00	11	1 1	XXXX . XX00	pqXX . XXXX	pqXX . vw03	No write
	• •					
01	00	1	ghij . klo1	pqrs . vwyz	pqrs . vw01	yagh . ijk1
01	01	1	ghij . klo1	pqrs . vwXX	pqrs . vw02	vwgh . ijk1
01	10	1	ghij . klo1	pqrs . XXXX	pqXX . vw03	ragh . ijk1
01	11	1	ghij . klo1	pqXX . XXXX	XXXX . XX00	pqgh . ijk1
10	00	1	ghij . XX02	pqrs . vwyz	pqrs . vw02	vwyz . ghij
10	01	1	ghij . XX02	pqrs . vwXX	pqXX . vw03	rsvw . ghij
10	10	1	ghij . XX02	pqrs . XXXX	XXXX . XX00	pqrs . ghij
10	11	1	ghij . XX02	pqXX . XXXX	pqgh . ij01	No write
11	00	1	ghXX . XX03	pqrs . vwyz	pqXX . vw03	rsvw . yzgh
11	01	1	ghXX . XX03	pqrs . vwXX	XXXX . XX00	pqrs . vwgh
11	10	11	ghXX . XX03	pqrs . XXXX	pqrs . gh01	No write
11	11	1	ghXX . XX03	pqXX . XXXX	pqgh . XX02	No write

OCTETS field in TX_ALIGN	TEXT_ALIGN register word at	word writting to FIFO register	FIFO register written	TEXT_ALIGN after FIFO register	Word written to FIFO
register	start		1.000.8883	write	
00	XXXX . XX00	pqrs . vwyz	TX . FIF00	XXXX . XX00	pqrs . vwyz
00	XXXX . XX00	XXrs . vwyz	TX . FIF01	rsvw . yz01	No write
00	XXXX . XX00	XXXX . vwyz	TX . FIFO2	vwzy . XX02	No write
00	XXXX . XX00	XXXX . XXyz	TX . FIFO3	yzXX . XX03	No write
			-1.55.7		V
01	ghij . klo1	pqrs . vwyz	TX . FIFO0	pqrs . vw01	yzgh . ijk1
01	ghij . klo1	XXrs . vwyz	TX . FIF01	rsvw . XX02	yzgh . ijk1
01	ghij . klo1	XXXX . vwyz	TX . FIFO2	vwXX . XX03	yzgh . ijk1
01	ghij . klo1	XXXX . XXyz	TX . FIFO3	XXXX . XX00	yzgh . ijk1
10	ghij . XX02	pqrs . vwyz	TX . FIFO0	pgrs . XX02	vwyz . ijk1
10	ghij . XX02	XXrs . vwyz	TX . FIFO1	rsXX . XX03	vwyz . ijk1
10	ghij . XX02	XXXX . vwyz	TX . FIFO2	XXXX . XX00	vwyz . ijk1
10	ghij . XX02	XXXX . XXyz	TX . FIFO3	pqgh . ij01	No write
			C-WCAA WY	1	
11	ghXX . XX03	pqrs . vwyz	TX . FIFO0	pqXX . XX03	rsvw . yzgh
11	ghXX . XX03	XXrs . vwyz	TX . FIFO1	XXXX . XX00	rsvw . yxgh
11	ghXX . XX03	XXXX . vwyz	TX . FIFO2	vwyz . gh01	No write
11	ghXX . XX03	XXXX . XXyz	TX . FIF03	yzgh . XX02	No write





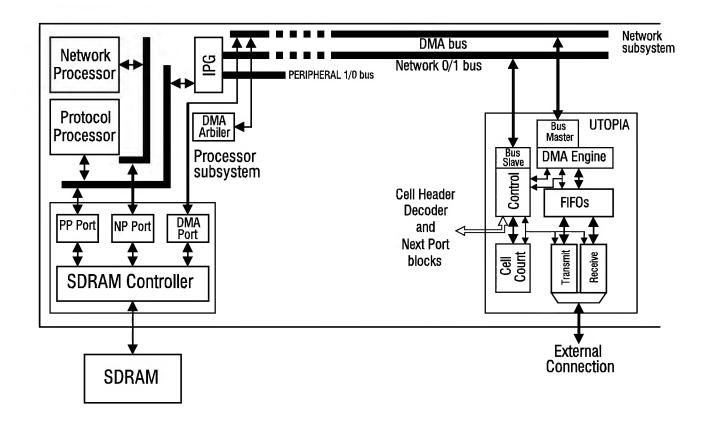


FIG. 14

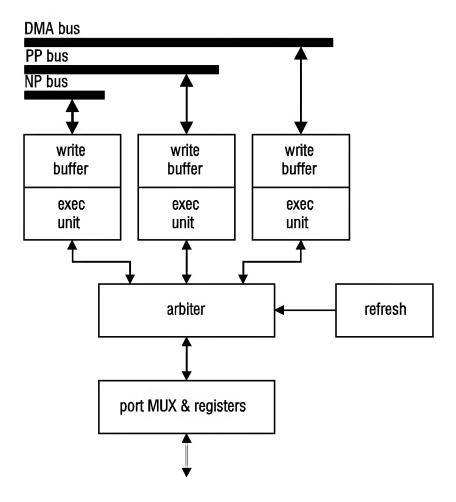
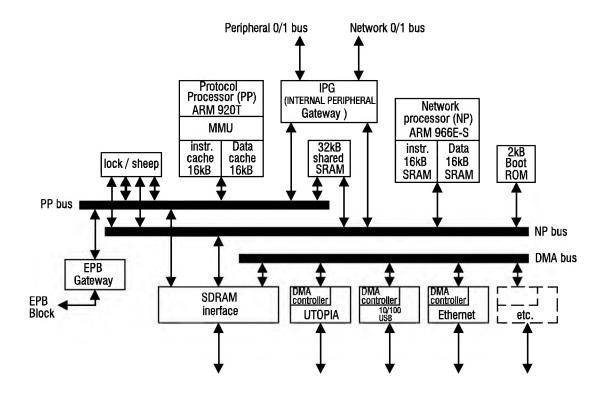


FIG. 15



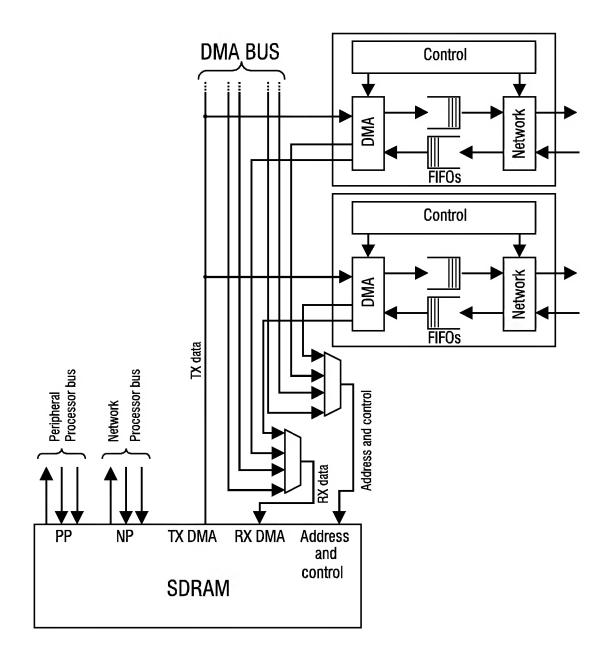


FIG. 17